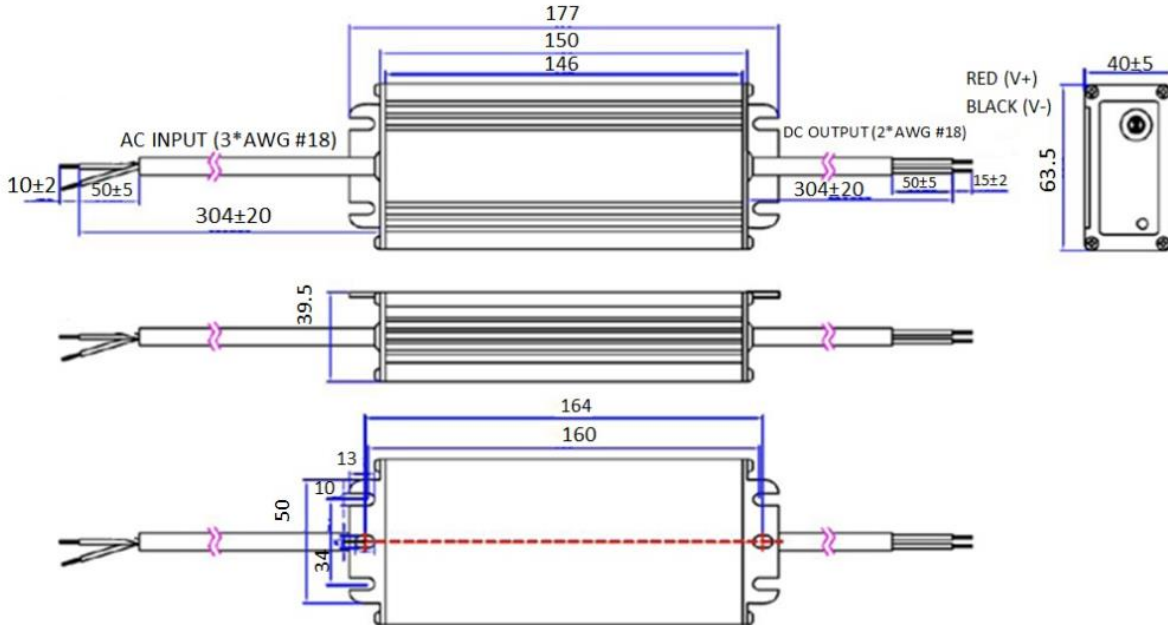


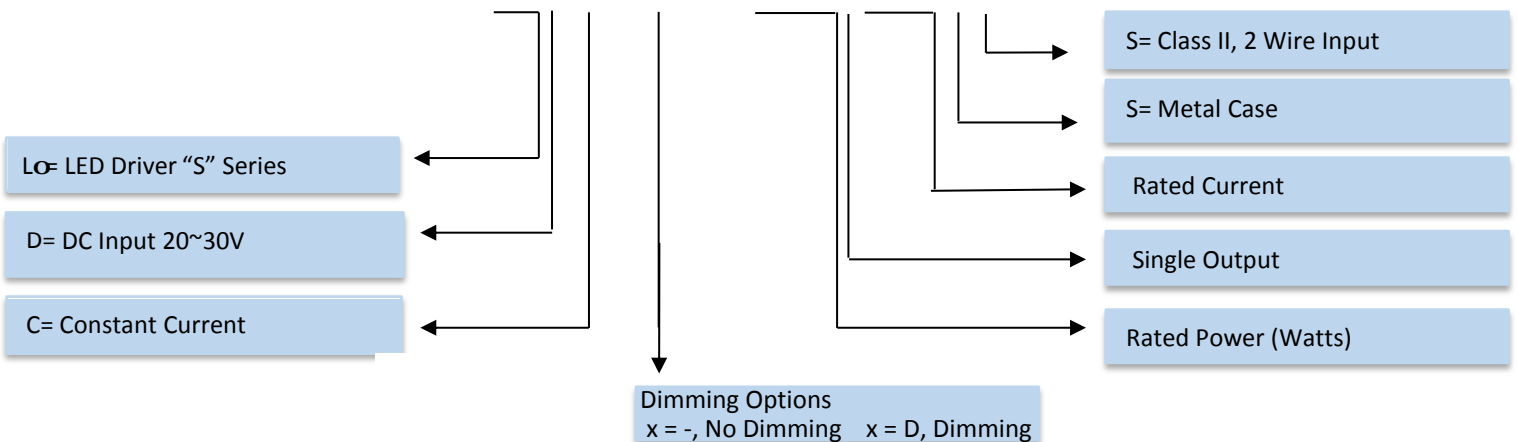
Notes :

1. All specifications are typical at 25°C unless otherwise stated.
2. The "Efficiency" values are measured at full load, after the unit is thermally stabilized, otherwise they will be lower about 1%.
3. The "Ripple & Noise" values are measured by 20MHz bandwidth oscilloscope and the output paralleled a 0.1uF ceramic capacitor and a 10uF electrolytic capacitor.



Part Number Scheme

LSDC(- or D) 100S210SS



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Data is based upon tests performed by AUTEC in a controlled environment and representative performance. Actual performance can vary depending on operating conditions. Specifications are subject to change without notice. All specifications are nominal unless otherwise noted

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